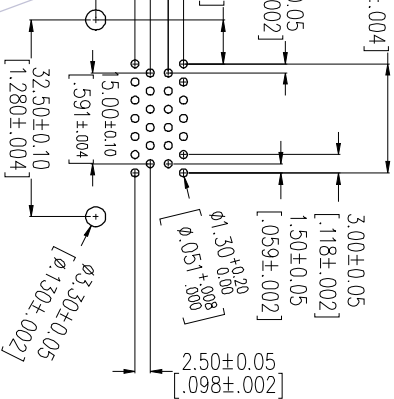
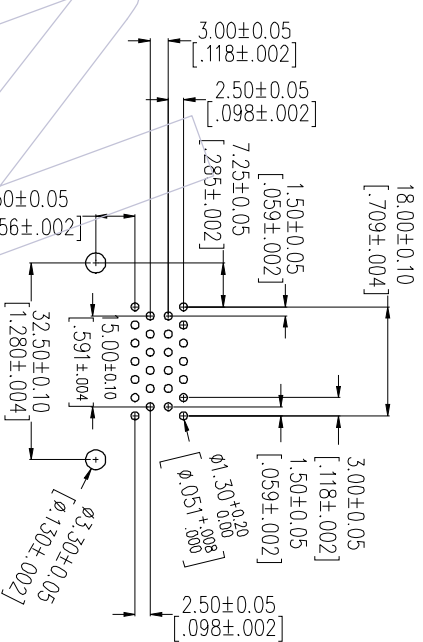


Recommended P.C.B Layout (Top Side)
(PCB BOARD TOLERANCE $\pm$ 0.05)

1.50+0.05 [1.18±.002] 1.50+0.05 [2]

 1.50 ± 0.05
 $[0.50 + 0.03]$ $[700 \pm 75 \text{ T. } 002]$

[.285 ± .002]

[illegible]

$\left(\begin{array}{c} \vdots \\ \vdots \\ \vdots \end{array} \right)$

4) _____

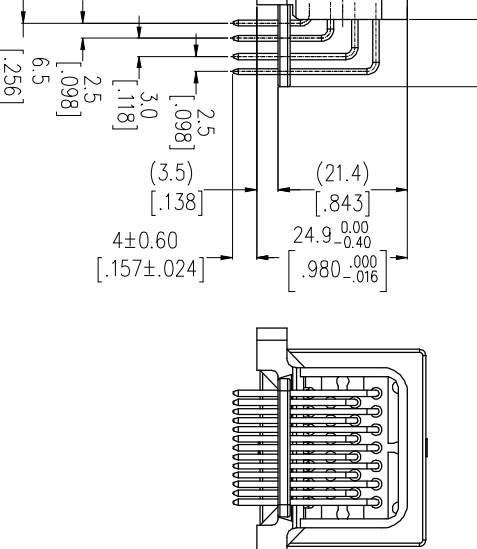
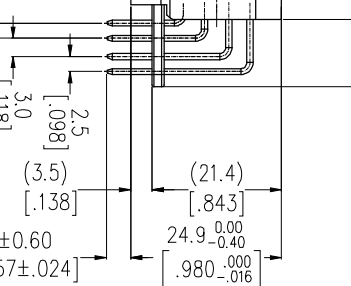
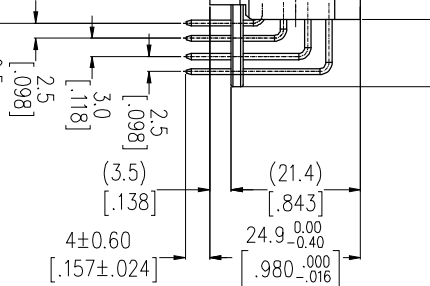
21.4

2.5

[860.]

(3)

[.18]



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